



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Qiyong Bian

Title: CO-SIMULATION OF VERILOG/PLI AND SYSTEM C MODULES USING REMOTE
PROCEDURE CALL

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Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

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Notice of Allowance Date:

April 20, 2006

We are transmitting herewith the attached:

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